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Atty. Dkt. No. 069974-0143

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Keiichiro KATA et al.

Title: PROCESS FOR
MANUFACTURING
SEMICONDUCTOR DEVICE AND
SEMICONDUCTOR WAFER

Appl. No.: Application for Reissue of US
Patent No. 5,844,304, granted
December 1, 1998

Examiner: TBD

Art Unit: TBD

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

ATTENTION: DRAWING REVIEW BRANCH

Sir:

Transmitted herewith are the formal drawings (4 sheets, Figures 1A-1C, 2A-2G, 3A-3G, 4A-4C) for the above-identified application. The Official Draftsperson is respectfully requested to approve these drawings for entry into the application.

Respectfully submitted,

Date 8/17/02

By



John J. Feldhaus
Attorney for Applicant
Registration No. 28,822

FOLEY & LARDNER
Customer Number: 22428



22428

PATENT TRADEMARK OFFICE

Telephone: (202) 672-5403
Facsimile: (202) 672-5399